

11050 U.S. PTO  
10/026053

2/21/01

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10026053	FILING DATE 12/21/2001	CLASS 324	SUBCLASS 765	GAU 2658	EXAMINER T. H. H. H.
----------------------	---------------------------	--------------	-----------------	-------------	-------------------------

**\*\*APPLICANTS:** Burns Mark;

**\*\*CONTINUING DATA VERIFIED:**  
JN (N/A)

**\*\* FOREIGN APPLICATIONS VERIFIED:**  
None

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO TI-33380
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	
Verified and Acknowledged Examiners's initials		TITLE : Parallel integrated circuit test apparatus and test method	

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH: ☐ DISK (CRF) ☐ CD-ROM  
(Attached in pocket on right inside flap)